सी एस आई आर—केन्द्रीय यांत्रिक अभियांत्रिकी अनुसंधान संस्थान
CSIR-CENTRAL MECHANICAL ENGINEERING RESEARCH INSTITUTE
(वैज्ञानिक तथा औद्योगिक अनुसंधान परिषद/Council of Scientific & Industrial Research)
महात्मा गांधी एवेन्यू, दुर्गापुर — 713 209/Mahatma Gandhi Avenue, Durgapur - 713 209

सं./No. 4/18/2020-Rct   दिनांक/Date : 12.12.2020

ADDENDUM
Advertisement No. 04/2020

Attention of all concerned is invited to the CSIR-CMERI Advertisement No. 04/2020 dated 08.12.2020 for the Walk-in-interview to be held on 16th and 17th December, 2020 for engagement as **Project-SRF/JRF/Project Associate /Project Assistant/CSIR-UGC JRF(NET)** under various projects. Additional position has been included in the already advertised positions of Project-SRF/JRF/Project Associate /Project Assistant/CSIR-UGC JRF(NET) as under.

Sl. No.	Post Code	Project No. / Sponsored Agency	Name of the Positions	No. of Positions	Educational Qualification and Experience	Tenure	Upper age limit	Consolidated Remuneration per month (Rs.)	Reporting time for Walk-In-Interview
27	P-4027	MLP-231512	Project Associate-I (Place of posting at CSIR-CMERI Durgapur Or CSIR-CMERI-CoEFM, Ludhiana)	02	BE / B.Tech. in Electronics / Instrumentation / Communication / Telecommunication Engineering from a recognized University or equivalent who are selected through (a) National Eligibility Test-CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions. Desirable (Any) : (1) Good knowledge/experience of industrial electronics product development instrumentation, hardware programming, signal processing, FPGA and IoT technologies. (2) Good knowledge/experience of Python, C, C++, practical Deep Learning, Machine Learning and AI Technologies, GPU programming, Computer Vision and Edge computing.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹31000/- (Offer of engagement will be issued subject to receipt of the fund)	17th December, 2020 at 9:30 AM to 11:00 AM
					OR				
					BE / B.Tech. in Electronics / Instrumentation / Communication / Telecommunication Engineering from a recognized University or equivalent. Desirable (Any) : (1) Good knowledge/experience of industrial electronics product development instrumentation, hardware programming, signal processing, FPGA and IoT technologies. (2) Good knowledge/experience of Python, C, C++, practical Deep Learning, Machine Learning and AI Technologies, GPU programming, Computer Vision and Edge computing.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹25000/- (Offer of engagement will be issued subject to receipt of the fund)	
28	P-4028	MLP-231512	Project Associate-I (Place of posting at CSIR-CMERI Durgapur Or CSIR-CMERI-CoEFM, Ludhiana)	01	BE / B.Tech. in Computer Science and Engineering / Information Technology from a recognized University or equivalent who are selected through (a) National Eligibility Test-CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions. Desirable (Any) : (1) Good knowledge/experience of Python, C, C++, practical Deep Learning, Machine Learning and AI Technologies, GPU programming, Computer Vision and Edge computing. (2) Good knowledge/experience of industrial electronics product development instrumentation, hardware programming, signal processing, FPGA and IoT technologies.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹31000/- (Offer of engagement will be issued subject to receipt of the fund)	17th December, 2020 at 9:30 AM to 11:00 AM
					OR				
					BE / B.Tech. in Computer Science and Engineering / Information Technology from a recognized University or equivalent. Desirable (Any) : (1) Good knowledge/experience of Python, C, C++, practical Deep Learning, Machine Learning, GPU programming, Computer Vision and Edge computing. (2) Good knowledge/experience of industrial electronics product development instrumentation, hardware programming, signal processing, FPGA and IoT technologies.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹25000/- (Offer of engagement will be issued subject to receipt of the fund)	

SI. No.	Post Code	Project No. / Sponsored Agency	Name of the Positions	No. of Positions	Educational Qualification and Experience	Tenure	Upper age limit	Consolidated Remuneration per month (Rs.)	Reporting time for Walk-In-Interview
29	P-4029	MLP-231512	Project Associate-II (Place of posting at CSIR-CMERI-CoEFM, Ludhiana Or CSIR-CMERI Durgapur)	01	BE / B.Tech. in Mechanical / Farm Machinery Engineering from a recognized University or equivalent who are selected through (a) National Eligibility Test-CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions with 2 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisations and Scientific activities and services. Desirable (Any) : (1) Good knowledge/Experience in design and development of industrial systems, Matlab/Simulink programming, CAD, Kinematic and dynamic analysis of multi-body systems, mathematical modeling of dynamic systems. (2) Good knowledge/ Experience in design and development of pneumatic system, process plant mechanism, farm machinery, mechanical system simulation, product realization and benchmarking.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹35000/- (Offer of engagement will be issued subject to receipt of the fund)	17th December, 2020 at 9:30 AM to 11:00 AM
					OR				
					BE / B.Tech. in Mechanical / Farm Machinery Engineering from a recognized University or equivalent with 2 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisations and Scientific activities and services. Desirable : (1) Good knowledge/Experience in design and development of industrial systems, Matlab/Simulink programming, CAD, Kinematic and dynamic analysis of multi-body systems, mathematical modeling of dynamic systems. (2) Good knowledge/ Experience in design and development of pneumatic system, process plant mechanism, farm machinery, mechanical system simulation, product realization and benchmarking.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹28000/- (Offer of engagement will be issued subject to receipt of the fund)	
30	P-4030	MLP-231512	Project Associate-II (Place of posting at CSIR-CMERI-CoEFM, Ludhiana Or CSIR-CMERI Durgapur)	01	BE / B.Tech. in Electronics / Instrumentation / Communication / Telecommunication Engineering from a recognized University or equivalent who are selected through (a) National Eligibility Test-CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions with 2 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisations and Scientific activities and services. Desirable (Any) : (1) Good knowledge/experience of industrial electronics product development instrumentation, hardware programming, signal processing, FPGA and IoT technologies. (2) Good knowledge/experience of Python, C, C++, practical Deep Learning, Machine Learning and AI Technologies, GPU programming, Computer Vision and Edge computing.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹35000/- (Offer of engagement will be issued subject to receipt of the fund)	17th December, 2020 at 9:30 AM to 11:00 AM
					OR				
					BE / B.Tech. in Electronics / Instrumentation / Communication / Telecommunication Engineering from a recognized University or equivalent with 2 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisations and Scientific activities and services. Desirable : (1) Good knowledge/experience of industrial electronics product development instrumentation, hardware programming, signal processing, FPGA and IoT technologies. (2) Good knowledge/experience of Python, C, C++, practical Deep Learning, Machine Learning and AI Technologies, GPU programming, Computer Vision and Edge computing.	01 Year (Extendable upto 31st March 2023 or co-terminus with the project)	35 Years	₹28000/- (Offer of engagement will be issued subject to receipt of the fund)	

All other contents of the Advertisement remain unchanged. The Selection process may continue till late evening/next day. Candidates are advised to come prepared accordingly.

(एस बनर्जी / S. Banerjee) 12/12/2020

अनुभाग अधिकारी / Section Officer